

03-10-03

2829

AMENDMENT TRANSMITTAL LETTER (Large Entity)

Applicant(s): Masahiro Ishida, et al.

Docket No.

02008.071001; AD-0243PCTUS

Serial No.
09/980,891Filing Date
December 3, 2001 MAR 0 2003Examiner
T. NguyenGroup Art Unit
2829

Invention:

METHOD FOR ANALYZING FAILURE OF SEMICONDUCTOR INTEGRATED CIRCUIT AND FAILURE

TO THE ASSISTANT COMMISSIONER FOR PATENTS:

Transmitted herewith is an amendment in the above-identified application.

The fee has been calculated and is transmitted as shown below.

CLAIMS AS AMENDED

	CLAIMS REMAINING AFTER AMENDMENT	HIGHEST # PREV. PAID FOR	NUMBER EXTRA CLAIMS PRESENT	RATE	ADDITIONAL FEE
TOTAL CLAIMS	31 -	31 =	0	x \$18.00	\$0.00
INDEP. CLAIMS	5 -	5 =	0	x \$84.00	\$0.00
Multiple Dependent Claims (check if applicable)		<input type="checkbox"/>			\$0.00
					TOTAL ADDITIONAL FEE FOR THIS AMENDMENT
					\$0.00

- No additional fee is required for amendment.
- Please charge Deposit Account No. in the amount of
A duplicate copy of this sheet is enclosed.
- A check in the amount of to cover the filing fee is enclosed.
- The Commissioner is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account No. 50-0591
A duplicate copy of this sheet is enclosed.
- Any additional filing fees required under 37 C.F.R. 1.16.
- Any patent application processing fees under 37 CFR 1.17.

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Signature

#45,079

Dated: March 7, 2003

Jonathan P. Osha; Reg. No.: 33,986

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I certify that this document and fee is being deposited on with the U.S. Postal Service as first class mail under 37 C.F.R. 1.8 and is addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.

Signature of Person Mailing Correspondence

Typed or Printed Name of Person Mailing Correspondence

CC:



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Masahiro ISHIDA, *et al.* Art Unit : 2829
Serial No.: 09/980,891 Examiner : Nguyen, Tung X.
Filed : December 3, 2001
Title : METHOD FOR ANALYZING FAILURE OF SEMICONDUCTOR
INTEGRATED CIRCUIT AND FAILURE

Assistant Commissioner for Patents
Washington, DC 20231

*6/Elect
G. Stanley
1-18-03*

REPLY UNDER 37 CFR § 1.111

In response to the Restriction Requirement dated February 12, 2003, Applicant elects Species I, on which claims 1, 2, 5-14, and 17-25 read, and which relates to Figures 20-24, 35, 37, and 39, for continued prosecution without traverse. Currently, claims 1, 6-12, and 18-24 are considered generic to Species I, II, and III. Thus, non-elected claims 3-4, 15-16, and 26-27 are left pending.

Applicant believes this reply to be fully responsive to all outstanding issues and places this application in condition for allowance. If this belief is incorrect, or other issues arise, do not hesitate to contact the undersigned or his associates at the telephone number listed below. Please apply any charges not covered, or any credits, to Deposit Account 50-0591 (Reference Number 02008.071001).

Respectfully submitted,

Date: 3/7/03


~~3/7/03~~ 445,079
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CERTIFICATE OF MAILING BY "EXPRESS MAIL" (37 CFR 1.10)
Applicant(s): Masahiro Ishida, et al.

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02008.071001;
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Serial No. 09/980,891	Filing Date December 3, 2001	Examiner T. Nguyen	Group Art Unit 2829
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Invention:

**METHOD FOR ANALYZING FAILURE OF SEMICONDUCTOR
INTEGRATED CIRCUIT AND FAILURE**

I hereby certify that the following correspondence:

Response to Office Action along with the required papers
(Identify type of correspondence)

is being deposited with the United States Postal Service "Express Mail Post Office To Addressee" service under 37 CFR 1.10 in an envelope addressed to: The Assistant Commissioner for Patents, Washington, D.C. 20231 on March 7, 2003.
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